



IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Inventor(S) Sailesh Chittipeddi Case 79
Serial No. 09/596382 Group Art Unit
Filing Date 6/16/2000 Examiner Not yet assigned
Title A Process For Manufacturing An Integrated Circuit Including A Dual-Damascene Structure And A Capacitor

THE COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D. C. 20231
SIR:

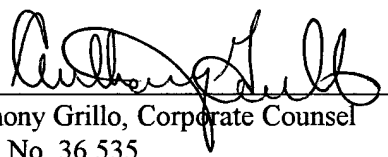
INFORMATION DISCLOSURE STATEMENT
CERTIFICATION UNDER 37 CFR 1.97

In accordance with 37 CFR 1.97 (b), the enclosed Information Disclosure Statement, with attached reference (s), is submitted for consideration in the above-identified application.

NO FEE REQUIRED.

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Agere Systems Inc. Deposit Account No. 50-1735 as required to correct the error.

Respectfully,

By 
Anthony Grillo, Corporate Counsel
Reg. No. 36,535
610-712-3766

Agere Systems Inc.

Date: 3/29/02

Att.
Information Disclosure Statement with attachment(s)

2812
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on 3/29/02 (Date of Deposit)

Mary Ellen Davis Date

#6/IDS.
10 ON/OW
4-17-02

RECEIVED
APR 10 2002
TC 2300 MAIL ROOM



INVESTOR IN PEOPLE

Application No: GB 0114308.0
Claims searched: All

Examiner: Michael R. Wendt
Date of search: 26 February 2002

Patents Act 1977 Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK CI (Ed.T): H1K (KFLS, KGAMS)

Int CI (Ed.7): H01L 21/02, 21/8242, 27/108

Other: Online: EPODOC, WPI, Japio

Documents considered to be relevant:

Category	Identity of document and relevant passage		Relevant to claims
P, A	GB 2356974 A	(LUCENT TECH.) e.g. see page 3 line 10 - page 6 line 25. Claims.	1 & 13
P, A	GB 2356973 A	(LUCENT TECH) e.g. see page 3 line 9 - page 6 line 19; Claims.	1 & 13
P, X	EP 1022783 A1	(LUCENT TECH.) e.g. see Column 3 lines 14 - 17; Column 6 lines 32 etc. Claims.	1 & 13 at least.
P, X	EP 1020905 A1	(LUCENT TECH.) e.g. Column 2 lines 43 - 46; Column 6 lines 21 etc; Claims.	-----"
X	US 6025226	(IBM) e.g. see Abstract; Column 2 lines 45 - 59; Column 3 lines 25 etc.	-----"

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.